

Receipt #3  
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4/6/00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

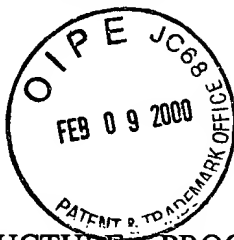
In re the Application of: Shigeo OHSAKA, et al.

Serial Number: 09/456,531

Group Art Unit: 2812

Filed: December 8, 1999

Attention: Appln Processing Division,  
Customer Correction Branch



For: ELECTRODE STRUCTURE, PROCESS FOR FABRICATING ELECTRODE  
STRUCTURE AND SEMICONDUCTOR LIGHT-EMITTING DEVICE **RECEIVED**

REQUEST FOR CORRECTED FILING RECEIPT

MAR 28 2000

TECHNOLOGY CENTER 2800

Assistant Commissioner for Patents  
Washington, D.C. 20231

Date: February 9, 2000

Sir:

Please supply the undersigned attorney with a corrected filing receipt for the above-identified application. The undersigned also respectfully requests that the Patent and Trademark Office records be amended to reflect the correction.

In reviewing the official Filing Receipt, we noted an error in that there is a typographical error in the title (a comma, --,-- should be inserted between "STRUCTURE" and "PROCESS"). A copy of the Declaration is enclosed which indicates the correct title.

We are enclosing a copy of the filing receipt with the corrections highlighted.

In the event that any fees are required in connection with this paper, please charge our Deposit Account No. 01-2340.

Respectfully submitted,

ARMSTRONG, WESTERMAN, HATTORI,  
MCLELAND & NAUGHTON

Mel R. Quintos  
Attorney for Applicants  
Reg. No. 31,898

Atty. Docket No. 991387  
1725 K Street, N.W., Suite 1000  
Washington, DC 20006  
Tel: (202) 659-2930  
Fax: (202) 887-0357  
MRQ/cs

Enclosures: Copies of Filing Receipt & Declaration

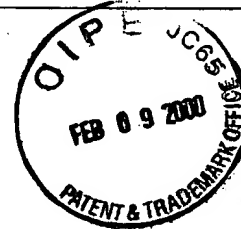
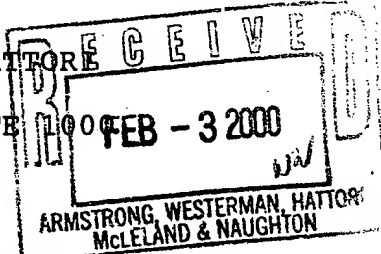
FILING RECEIPT



UNITED STATES DEPARTMENT OF COMMERCE  
Patent and Trademark Office  
ASSISTANT SECRETARY AND COMMISSIONER  
OF PATENTS AND TRADEMARKS  
Washington, D.C. 20231

APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTORNEY DOCKET NO.	DRWGS	TOT CL	IND CL
09/456,531	12/08/99	2812	\$994.00	991387	14	18	6

ARMSTRONG WESTERMAN HATTORI  
MCLELAND & NAUGHTON  
1725 K STREET N W SUITE 1100  
WASHINGTON DC 20006



Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts of Application" ("Missing Parts Notice") in this application, please submit any corrections to this Filing Receipt with your reply to the "Missing Parts Notice." When the PTO processes the reply to the "Missing Parts Notice," the PTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s) SHIGEO OHSAKA, YAMANASHI-KEN, JAPAN; SHINICHI DOMOTO,  
YAMANASHI-KEN, JAPAN; NOBUMASA OKADA, YAMANASHI-KEN,  
JAPAN.

FOREIGN APPLICATIONS- JAPAN 11-73500/1999 03/18/99

IF REQUIRED, FOREIGN FILING LICENSE GRANTED 01/24/00  
TITLE

ELECTRODE STRUCTURE, PROCESS FOR FABRICATING ELECTRODE STRUCTURE AND  
SEMICONDUCTOR LIGHT-EMITTING DEVICE

PRELIMINARY CLASS: 438

*Please insert "3."*

DATA ENTRY BY: BLIZZARD, SHARON .L TEAM: 01 DATE: 01/24/00

|||||

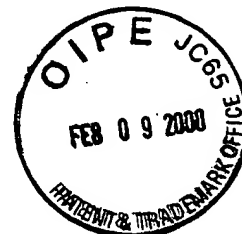
(See reverse for new important information)

## Declaration and Power of Attorney for U.S. Patent Application

特許出願宣言書及び委任状

Japanese Language Declaration

日本語宣言書



下記の氏名の発明者として、私は以下の通り宣言します。

As a below named inventor, I hereby declare that:

私の住所、私書箱、国籍は下記の私の氏名の後に記載された通りです。

My residence, post office address and citizenship are as stated next to my name.

下記の名称の発明に関して請求範囲に記載され、特許出願している発明内容について、私が最初かつ唯一の発明者（下記の氏名が一つの場合）もしくは最初かつ共同発明者であると（下記の名称が複数の場合）信じています。

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

ELECTRODE STRUCTURE, PROCESS FOR  
FABRICATING ELECTRODE STRUCTURE  
AND SEMICONDUCTOR LIGHT-EMITTING  
DEVICE

上記発明の明細書（下記の欄でx印がついていない場合は、本書に添付）は、

the specification of which is attached hereto unless the following box is checked:

☐ 月 日に提出され、米国出願番号または特許協定条約国際出願番号を \_\_\_\_\_ とし、  
（該当する場合） \_\_\_\_\_ に訂正されました。

☐ was filed on \_\_\_\_\_  
as United States Application Number or  
PCT International Application Number  
\_\_\_\_\_ and was amended on  
\_\_\_\_\_ (if applicable).

私は、特許請求範囲を含む上記訂正後の明細書を検討し、内容を理解していることをここに表明します。

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

私は、連邦規則法典第37編第1条56項に定義されるとおり、特許資格の有無について重要な情報を開示する義務があることを認めます。

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

**FILE COPY**

Bib Data Sheet

**UNITED STATES DEPARTMENT OF COMMERCE  
Patent and Trademark Office**Address: COMMISSIONER OF PATENTS AND TRADEMARKS  
Washington, D.C. 20231

<b>SERIAL NUMBER</b> 09/456,531	<b>FILING DATE</b> 12/08/1999 <b>RULE</b> _	<b>CLASS</b> 438	<b>GROUP ART UNIT</b> 2812	<b>ATTORNEY DOCKET NO.</b> 991387
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**APPLICANTS**  
SHIGEO OHSAKA, YAMANASHI-KEN, JAPAN;  
SHINICHI DOMOTO, YAMANASHI-KEN, JAPAN;  
NOBUMASA OKADA, YAMANASHI-KEN, JAPAN;

**\*\* CONTINUING DATA \*\*\*\*\*** *None*

**\*\* FOREIGN APPLICATIONS \*\*\*\*\*** *IN*  
JAPAN 11-73500/1999 03/18/1999

**IF REQUIRED, FOREIGN FILING LICENSE GRANTED \*\***  
**\*\* 01/24/2000**

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	<b>STATE OR COUNTRY</b> JAPAN	<b>SHEETS DRAWING</b> 14	<b>TOTAL CLAIMS</b> 18	<b>INDEPENDENT CLAIMS</b> 6
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials				

**ADDRESS**  
ARMSTRONG WESTERMAN HATTORI  
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WASHINGTON ,DC 20006

**TITLE**  
ELECTRODE STRUCTURE, PROCESS FOR FABRICATING ELECTRODE STRUCTURE AND SEMICONDUCTOR LIGHT-EMITTING DEVICE

<b>FILING FEE RECEIVED</b> 994	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
		<input type="checkbox"/> 1.16 Fees ( Filing )
		<input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )
		<input type="checkbox"/> 1.18 Fees ( Issue )
		<input type="checkbox"/> Other _____
		<input type="checkbox"/> Credit